

of the semiconductor microrelay in FIG. 41;

FIG. 50 is a relation drawing used to describe the function of the semiconductor microrelay in FIG. 41;

FIG. 51 is a relation drawing used to describe the function  
5 of the semiconductor microrelay in FIG. 41;

FIG. 52 is a partially cutaway view in perspective of the structure of another semiconductor microrelay;

FIG. 53 is a top view to show the structure of a semiconductor microactuator in a related art;

10 FIG. 54 is a sectional view to show the structure of the semiconductor microactuator in the related art;

FIG. 55 is a sectional view to show the structure of a semiconductor microrelay in a related art; and

FIG. 56 is a schematic drawing used to describe the function  
15 of the semiconductor microrelay in the related art.

FIG. 57 is a partially cutaway view in perspective of the structure of a semiconductor microactuator using a semiconductor device corresponding to another embodiment of the invention;

FIG. 58 (b) is a sectional view to show the structure of the  
20 semiconductor microactuator in FIG. 57;

FIG. 58(b) is a top view to show the structure of the semiconductor microactuator in FIG. 57;

FIG. 59 is a partially cutaway view in perspective of the structure of a semiconductor microactuator using a semiconductor  
25 device corresponding to another embodiment of the invention;